

## WEST Search History

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DATE: Monday, March 26, 2007

<u>Hide?</u>	<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>				
<input type="checkbox"/>	L53		l49 and L52	4
<input type="checkbox"/>	L52		712/239.ccls.	488
<input type="checkbox"/>	L51		l49 and L50	3
<input type="checkbox"/>	L50		712/233.ccls.	346
<input type="checkbox"/>	L49		712/209.ccls.	357
<input type="checkbox"/>	L48		(stream\$1 with mesocode) and L43	1
<input type="checkbox"/>	L47		(mesocode same stream) and l43	1
<input type="checkbox"/>	L46		(stream with two) and l43	1
<input type="checkbox"/>	L45		(stream with intermediate code) and l43	0
<input type="checkbox"/>	L44		(stream with mesocode) and L43	0
<input type="checkbox"/>	L43		(implement mesocode).ti.	1
<input type="checkbox"/>	L42		l39 not L41	29
<input type="checkbox"/>	L41		l39 not L40	59
<input type="checkbox"/>	L40		intel.as.	15452
<input type="checkbox"/>	L39		stream\$1 same L37	88
<input type="checkbox"/>	L38		stream same L37	88
<input type="checkbox"/>	L37		l30 with l31	920
<input type="checkbox"/>	L36		branch\$3 with L35	10
<input type="checkbox"/>	L35		stream\$3 mode\$1	1756
<input type="checkbox"/>	L34		stream with termination with branch\$3 and stream with initialization with branch\$3	2
<input type="checkbox"/>	L33		stream with termination with L32 and stream with initialization with L32	4
<input type="checkbox"/>	L32		L30 or instruction\$1 or L31	591472
<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>				
macroinstruction\$1 or macro-instruction\$1 or macrocode or macro-code or				
<input type="checkbox"/>	L31		macro-operation\$1 or macrooperation\$1 or macro-op\$1 or macroop\$1 or macro-program\$1 or macroprogram\$1 or cisc instruction	1740
<input type="checkbox"/>	L30		firmware or nanoinstruction\$1 or nano-instruction\$1 or nanocode or nano-code or nanoprogram or nano-program or L29	98252
<input type="checkbox"/>	L29		microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro-program\$1 or microprogram\$1	20673

*DB=PGPB,USPT; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L28	stream with termination and stream with initialization	55
<input type="checkbox"/>	L27	mesocode and L26	2
<input type="checkbox"/>	L26	mesocode.ti,ab,clm.	2
<input type="checkbox"/>	L25	L19 and mesocode.ti,ab,clm.	0
<input type="checkbox"/>	L24	sham.in. and L22	7
<input type="checkbox"/>	L23	L20 and L22	1
<input type="checkbox"/>	L22	datta.in. and L19	65
<input type="checkbox"/>	L21	(exception\$1 same (32 bit with 64 bit)) and L19	3
<input type="checkbox"/>	L20	(32 bit with 64 bit) and L19	324
<input type="checkbox"/>	L19	intel.as.	15452

*DB=USPT,PGPB; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L18	('5381533'  '5579493'  '6145076'  '6304962'  '6347383'  '6598155')![pn]	6
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*DB=PGPB,USPT; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L17	6988190.pn.	1
<input type="checkbox"/>	L16	6304962.uref.	3
<input type="checkbox"/>	L15	6304962.pn.	1
<input type="checkbox"/>	L14	branch\$3 with L13	11
<input type="checkbox"/>	L13	stream buffer\$1	2116
<input type="checkbox"/>	L12	L11 same (write back or writeback)	7
<input type="checkbox"/>	L11	L10 with predict\$3	570
<input type="checkbox"/>	L10	stream\$1 with branch\$3	7342
<input type="checkbox"/>	L9	L8 not L7	3
<input type="checkbox"/>	L8	trace cache same (write back or writeback)	9
<input type="checkbox"/>	L7	trace cache with (write back or writeback)	6
<input type="checkbox"/>	L6	trace cache with decod\$3	151
<input type="checkbox"/>	L5	trace cache	381
<input type="checkbox"/>	L4	L3 not L2	16
<input type="checkbox"/>	L3	branch\$3 same L1	24
<input type="checkbox"/>	L2	branch\$3 with L1	8
<input type="checkbox"/>	L1	combin\$5 with basic block\$1	182

END OF SEARCH HISTORY